

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2491	710/52,53,107.ccls.	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/11/24 15:30
L2	1113	(separat\$3 divid\$3 segregat\$3 split\$4) near5 (common shared) near5 bus\$3	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/11/24 15:30
L4	35	1 and 2	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/11/24 15:30
L5	801	(separat\$3 individual\$2 different distinct) near3 address adj bus\$3	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/11/24 15:36
L6	174	L5 and (address adj bus\$3) near4 (port buffer interface)	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/11/24 15:37
L7	14	6 and 1	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/11/24 15:37
S1	615	(two second)near3 (address adj bus\$3)with(processor micro\$1processor controller micro\$1controller)	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/01/13 11:04
S2	701	(two second)near3 (address adj bus\$3)with(processor micro\$1processor controller micro\$1controller cpu)	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/01/13 12:16
S3	37	(two second)near3 (address adj bus\$3)with(processor micro\$1processor controller micro\$1controller cpu)with (separate different distinct)	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/01/13 13:00

S4	200	(separate different distinct)near3 (address adj bus\$3)with(processor micro\$1processor controller micro\$1controller cpu)	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/01/13 13:00
S5	180	S4 not S3	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/01/13 16:07
S6	17	("3906457" "4818932" "4847758" "4961172" "5001671" "5226010" "5386579" "5404464" "5606714" "5652847" "5655142" "5664140" "5680567" "5682498" "5813038" "6026466" "6209049").PN.	USPAT	OR	ON	2004/01/13 14:11
S7	3	710/52,53,107.ccls. and S4	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/01/13 16:10
S8	21	710/52,53,107.ccls. and S2	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/11/24 15:30
S9	0	("busadj(separat\$3orisolat\$3orseg regat\$3ordivi\$4)").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2003/08/07 15:02
S10	2521	bus adj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4)	US-PGPUB; USPAT	OR	ON	2003/08/07 15:05
S11	1585	(bus adj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4)) and (interfac\$3 same bus)	US-PGPUB; USPAT	OR	ON	2003/08/07 15:07
S12	248	((bus adj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4)) and (interfac\$3 same bus)) and RISC	US-PGPUB; USPAT	OR	ON	2003/08/07 15:07
S13	11	((((bus adj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4)) and (interfac\$3 same bus)) and RISC) and dsl	US-PGPUB; USPAT	OR	ON	2003/08/07 15:09
S14	11	((((bus adj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4)) and (interfac\$3 same bus)) and RISC) and ?dsl	US-PGPUB; USPAT	OR	ON	2003/08/14 14:59

S15	185	((bus adj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4)) and (interfac\$3 same bus)) and RISC) and command	US-PGPUB; USPAT	OR	ON	2003/08/07 15:10
S16	0	((((bus adj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4)) and (interfac\$3 same bus)) and RISC) and command) and PCM	US-PGPUB; USPAT	OR	ON	2003/08/07 15:10
S17	13	((bus adj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4)) and (interfac\$3 same bus)) and RISC) and PCM	US-PGPUB; USPAT	OR	ON	2003/08/07 15:23
S18	193	toroid\$3 with memory	US-PGPUB; USPAT	OR	ON	2003/08/11 11:34
S19	1	(toroid\$3 with memory) and (PCM or (pulse adj code adj modulat\$3))	US-PGPUB; USPAT	OR	ON	2003/08/11 11:33
S20	32	(toroid\$3 with memory) and fram\$3	US-PGPUB; USPAT	OR	ON	2003/08/11 13:52
S21	1	"6341335".pn.	US-PGPUB; USPAT	OR	ON	2003/08/11 13:53
S22	4625	(first adj bus)and (second adj bus)	US-PGPUB; USPAT	OR	ON	2003/08/11 14:48
S23	162	((first adj bus)and (second adj bus)) and (PCM or (pulse adj code adj modulat\$3))	US-PGPUB; USPAT	OR	ON	2003/08/11 14:49
S24	11	((((first adj bus)and (second adj bus)) and (PCM or (pulse adj code adj modulat\$3))) and Dsl	US-PGPUB; USPAT	OR	ON	2003/08/11 14:50
S25	106	((((first adj bus)and (second adj bus)) and (PCM or (pulse adj code adj modulat\$3))) and compar\$5	US-PGPUB; USPAT	OR	ON	2003/08/11 14:52
S26	101	(((((first adj bus)and (second adj bus)) and (PCM or (pulse adj code adj modulat\$3))) and compar\$5) and (port or buffer)	US-PGPUB; USPAT	OR	ON	2003/08/11 15:22
S27	58	data adj memory adj address adj bus	US-PGPUB; USPAT	OR	ON	2003/08/11 15:23
S28	792	advantage same (bus with (separat\$3 or split or divide))	US-PGPUB; USPAT	OR	ON	2003/08/11 16:00
S29	62	(advantage same (bus with (separat\$3 or split or divide))) and (address adj bus) same advantage	US-PGPUB; USPAT	OR	ON	2003/08/11 16:00
S30	34764	(input adj (port or interface)) and (output adj (port or interface))	US-PGPUB; USPAT	OR	ON	2003/08/13 07:59

S31	814	((input adj (port or interface)) and (output adj (port or interface))) and frame adj (detect\$3 or loca\$7 of find\$3 or identif\$7 or determin\$3)	US-PGPUB; USPAT	OR	ON	2003/08/13 11:01
S32	623	((((input adj (port or interface)) and (output adj (port or interface))) and frame adj (detect\$3 or loca\$7 of find\$3 or identif\$7 or determin\$3)) and (frame same input)	US-PGPUB; USPAT	OR	ON	2003/08/13 08:13
S33	43	(((((input adj (port or interface)) and (output adj (port or interface))) and frame adj (detect\$3 or loca\$7 of find\$3 or identif\$7 or determin\$3)) and (frame same input)) and (output same (PCM or (pulse adj code adj modulat\$3)))	US-PGPUB; USPAT	OR	ON	2003/08/13 08:13
S34	3471	output with PCM	US-PGPUB; USPAT	OR	ON	2003/08/13 11:00
S35	271	(output with PCM) and ((separate or distinct or different) with register)	US-PGPUB; USPAT	OR	ON	2003/08/13 11:00
S36	133	((output with PCM) and ((separate or distinct or different) with register)) and (frame with (detect\$3 or loca\$7 of find\$3 or identif\$7 or determin\$3))	US-PGPUB; USPAT	OR	ON	2003/08/13 11:02
S37	8	((((output with PCM) and ((separate or distinct or different) with register)) and (frame with (detect\$3 or loca\$7 of find\$3 or identif\$7 or determin\$3))) and (data adj pattern)	US-PGPUB; USPAT	OR	ON	2003/08/13 11:08
S38	27	((((output with PCM) and ((separate or distinct or different) with register)) and (frame with (detect\$3 or loca\$7 of find\$3 or identif\$7 or determin\$3))) and (data adj (pattern or sequence or arrangement))	US-PGPUB; USPAT	OR	ON	2003/08/13 12:47
S39	19	(input adj (buffer or port)) same xDSL	US-PGPUB; USPAT	OR	ON	2003/08/13 12:52
S40	130	(input adj (buffer or port)) and xDSL	US-PGPUB; USPAT	OR	ON	2003/08/14 13:03
S41	1	"5400369".pn.	US-PGPUB; USPAT	OR	ON	2003/08/14 15:16

S42	15	710/52.ccls. and ((bus adj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4)) and (interfac\$3 same bus))	US-PGPUB; USPAT	OR	ON	2003/08/14 15:03
S43	15	710/52.ccls. and (bus adj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4)) and (interfac\$3 same bus)	US-PGPUB; USPAT	OR	ON	2003/08/14 15:02
S44	13	DSL and (frame adj align\$3)	US-PGPUB; USPAT	OR	ON	2003/08/14 14:58
S45	0	DSL adj frame adj align\$3	US-PGPUB; USPAT	OR	ON	2003/08/14 13:04
S46	177	micro\$1processor near3 two near5 address	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/01/12 16:59
S47	32	(micro\$1processor near3 two near5 address) and (address adj bus)with two	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/01/12 17:07
S48	53	(processor micro\$1processor controller micro\$1controller)near5 (two adj address)with bus\$3	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/01/12 17:31
S49	158	(processor micro\$1processor controller micro\$1controller)near5 (second adj address)with bus\$3	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/01/12 17:32
S50	158	((processor micro\$1processor controller micro\$1controller)near5 (second adj address)with bus\$3) not ((micro\$1processor near3 two near5 address) and (address adj bus)with two)	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/01/12 17:32
S51	157	((processor micro\$1processor controller micro\$1controller)near5 (second adj address)with bus\$3) not ((processor micro\$1processor controller micro\$1controller)near5 (two adj address)with bus\$3)	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/01/12 17:32
S57	658	(separat\$3 individual\$2) near3 address adj bus\$3	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 16:17

S58	283	S57 and address adj bus\$3 with (port buffer interface)	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 16:18
S59	801	(separat\$3 individual\$2 different distinct) near3 address adj bus\$3	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 16:17
S60	327	S59 and address adj bus\$3 with (port buffer interface)	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 16:18
S61	174	S59 and (address adj bus\$3) near4 (port buffer interface)	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 16:19
S62	1113	(separat\$3 divid\$3 segregat\$3 split\$4) near5 (common shared) near5 bus\$3	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/11/24 15:30
S63	9	S61 and S62	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 16:21